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Patent Application

Inventor(s): Monte Mar and Warren Snyder

Serial No.: 09/929,891

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Examiner:

Title: PROGRAMMING ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM  
(AS AMENDED)

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<i>Mar</i>	A	6,144,327	11/07/00	Distinti et al.	341	126	08/12/97
<i>Mar</i>	B	5,202,687	04/13/93	Distinti	341	158	06/12/91
	C						
	D						
	E						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	F							
	G							
	H							

Other Documents

Examiner Initial	No.	Docket Number, Title, Filing Date, Serial Number & Inventors
<i>Mar</i>	I	CYPR-CD00169; "PROGRAMMABLE MICROCONTROLLER ARCHITECTURE (MIXED ANALOG/DIGITAL)"; 08/07/01; 09/924,734; Snyder et al.
	J	CYPR-CD00173; "A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE"; 07/18/01; 09/909,047; M. Mar
	K	CYPR-CD00174; "PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM"; 08/14/01; 09/930,021; Mar et al.
		CYPR-CD00232; "PROGRAMMABLE SYSTEM ON A CHIP"; 10/01/01; 10/033,027; Snyder
Examiner <i>Mar</i>		Date Considered 9/2004

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.